and linear polarization by simply changing the orientation angle of dipole. Circularly polarized antenna exhibits higher bandwidth due to asymmetry in the HIS. Linearly polarized antenna shows better cross-polar levels as compared with that designed using symmetrical HIS.

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# A FRACTAL-BASED COMPACT BROADBAND POLARIZATION INSENSITIVE METAMATERIAL ABSORBER USING LUMPED RESISTORS

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**ABSTRACT:** In this article, a broadband polarization insensitive metamaterial absorber has been proposed for C-band (4–8 GHz) applications using lumped resistors. The unit cell of the proposed structure is based on an inverted Minkowski fractal loop, where four lumped resistors are mounted to obtain a broad absorption band at the expense of 5 mm thick dielectric substrate, which is only  $0.033\lambda_0$  with respect to the lowest operating frequency. The proposed structure is polarization insensitive and also wide angle absorptive. A prototype of the proposed structure has been fabricated, which is experimentally verified with the simulated responses. The designed structure is much compact compared with other reported structures due to its unique design and thus can be considered as a compact broadband absorber for C-band applications. © 2016 Wiley Periodicals, Inc. Microwave Opt Technol Lett 58:343–347, 2016; View this article online at wileyonlinelibrary.com. DOI 10.1002/ mop.29571

**Key words:** *metamaterial absorber; broadband; lumped resistors; compact* 

## 1. INTRODUCTION

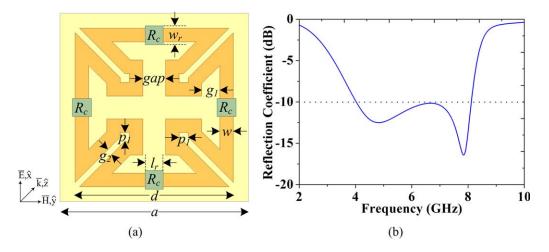
Day-by-day, there is a tremendous advancement in the utilization of metamaterial characteristics in real time applications like manufacturing of perfect lens [1], cloaking [2], antenna miniaturization [3], absorber [4], and so forth. Because of the unusual electromagnetic (EM) properties of the metamaterials, they are preferred in several applications in various frequency ranges starting from microwave to optical domain. Commercial wedgeand pyramidal-shaped microwave absorbers, used for EM absorption, require large spaces as well as they are bulky [5]. Therefore, there is always a search for ultrathin and light-weight EM absorbers, where the metamaterial based absorbers have come into existence. As the first metamaterial based absorber has been experimentally demonstrated in 2008 by Landy et al. [4], various designs on metamaterial absorbers have been investigated exhibiting different characteristics, such as single-band, dual-band, as well as broadband operations in different spectral ranges [6-10].

A metamaterial absorber generally consists of a periodic array of unit cells, formed by a dielectric substrate sandwiched between the top metallic pattern and bottom ground plane. The structure produces equivalent inductances and capacitances depending on the metallic patches and the gaps between the patches, respectively, which give rise to LC equivalent circuit. This causes dips in reflection and thereby near-unity absorption is obtained at resonance frequencies. As these metamaterial absorber structures are mainly based on resonance mechanism, the structures suffer from narrow absorption bandwidths. Hence many methods like multilayer [11], array of scaled unit cells [12], use of resistive sheet [13], mounting of lumped elements [14] have been proposed to achieve wideband and broadband absorption. Each method has its own advantages and disadvantages. Among them the simpler and cost effective one is the use of lumped resistors, where by appropriately choosing the resistor values, broad absorption band can be realized.

In this article a polarization insensitive and wide angle absorptive broadband absorber has been proposed, which comprises of inverted Mikowski fractal geometry mounted with lumped resistors. The structure exhibits 10-dB absorption bandwidth over the frequency range of 4-8.12 GHz under normal incidence with peak absorptions of 94.4% and 97.7% at 4.82 GHz and 7.84 GHz, respectively. The equivalent circuit model has been drawn to understand the absorption phenomenon. The reflectivity responses of the structure for oblique incidence and for different polarization angles under normal incidence show that the proposed absorber is polarization insensitive in nature as well as it maintains good absorption for angle of incidence upto 60°. Additionally, the structure is fabricated and the experimental responses have been verified with the simulated results. Last, the designed broadband absorber has been compared with other reported structures to show its advantage in terms of its compactness.

#### 2. DESIGN OF THE STRUCTURE

The top layer of the unit cell consists of a first-order inverted Minkowski fractal geometry, which is imprinted on a grounded FR-4 dielectric substrate ( $\varepsilon_r = 4.4$  and tan  $\delta = 0.02$ ) of 5 mm thickness as shown in Figure 1(a). Both the top and bottom layers are made of copper ( $\sigma = 5.8 \times 10^7$  S/m) of 0.035 mm thickness. The geometric dimensions of the unit cell are as follows: a = 12.5, d = 10.5, w = 0.9,  $g_1 = 1.2$ , P = 0.6,  $g_2 = 0.4$ , gap = 1.5, while the dimensions of chip resistors ( $R_C$ ) are  $w_r = 1.25$  and  $l_r = 1.2$  (all are in mm). The chip resistor



**Figure 1** (a) Front view of the unit cell geometry with geometric dimensions: a = 12.5, d = 10.5, w = 0.9,  $g_1 = 1.2$ , P = 0.6,  $g_2 = 0.4$ , gap = 1.5,  $w_r = 1.25$ , and  $l_r = 1.2$  (units: mm), (b) reflectivity of the simulated structure. [Color figure can be viewed in the online issue, which is available at wileyonlinelibrary.com]

dimensions are chosen according to technical data sheet of the resistors used in fabricated sample. The lumped resistors have the value of 150  $\Omega$ . The direction of incident EM wave is normal to the plane of unit cell geometry, which along with the electric and magnetic field vector directions are shown in Figure 1(a).

The structure is simulated in finite element method-based EM solver Ansys HFSS using periodic boundary conditions. Figure 1(b) shows the reflectivity of the structure under normal incidence, where the reflection coefficient is less than -10 dB over the frequency range 4–8.12 GHz covering the entire C-band. Two reflection dips are also observed at frequencies 4.82 GHz and 7.84 GHz with reflection coefficients of -12.5 dB and -16.42 dB, respectively.

When an EM wave is incident on a structure with complete copper backing, the relation between absorptivity and reflectance can be written as in (1):

$$A(\omega) = 1 - |S_{11}(\omega)|^2 \tag{1}$$

where  $A(\omega)$ ,  $|S_{11}(\omega)|^2$  are the absorbance and reflectance, respectively, at an angular frequency  $\omega$ . This reflection coefficient can be calculated from (2):

$$S_{11} = \frac{Y_0 - Y_{\rm in}}{Y_0 + Y_{\rm in}} \tag{2}$$

where  $Y_0$  and  $Y_{in}$  are the admittance of air and the input admittance of the structure, respectively, which are shown in the equivalent circuit representation of the structure designated as Figure 2(a).

This equivalent circuit of the proposed absorber consists of a frequency selective surface (FSS) based metallic pattern on the top surface, which can be modelled as a RLC series circuit and a transmission line formed by the dielectric substrate terminated by metal ground plane. Therefore, the input admittance of the structure can be represented as follows:

$$Y_{\rm in} = Y_{\rm FSS} + Y_{\rm d} \tag{3}$$

where

$$Y_{\text{FSS}} = \frac{1}{R_{\text{eff}} + j\omega L_{\text{eff}} - \frac{j}{\omega C_{\pi}}} = G + jB \tag{4}$$

$$G = \frac{\omega^2 R_{\rm eff} C^2_{\rm eff}}{\omega^2 R^2_{\rm eff} C^2_{\rm eff} + \left(1 - \omega^2 L_{\rm eff} C_{\rm eff}\right)^2} \tag{5}$$

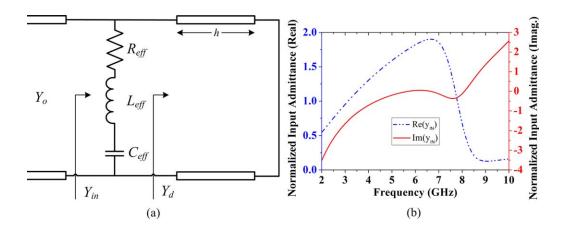


Figure 2 (a) Equivalent circuit model and (b) normalized input admittance of the proposed structure. [Color figure can be viewed in the online issue, which is available at wileyonlinelibrary.com]

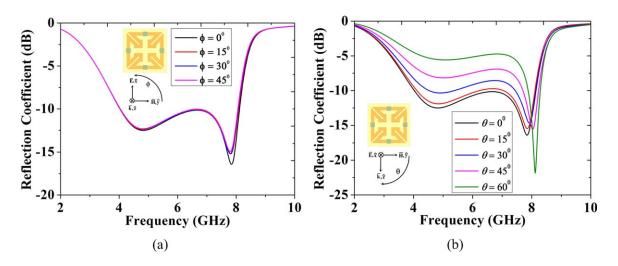


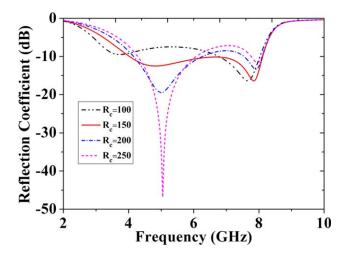
Figure 3 Reflectivity of the simulated structure (a) for different polarization angles of incident wave under normal incidence and (b) for different incident angles under TE polarization. [Color figure can be viewed in the online issue, which is available at wileyonlinelibrary.com]

$$B = \frac{\omega C_{\rm eff} (1 - \omega^2 L_{\rm eff} C_{\rm eff})}{\omega^2 R^2_{\rm eff} C^2_{\rm eff} + (1 - \omega^2 L_{\rm eff} C_{\rm eff})^2} \tag{6}$$

$$Y_{\rm d} = -j\sqrt{\varepsilon_0\varepsilon_{\rm r}/\mu_0\mu_{\rm r}}\cot(kh) \tag{7}$$

where  $\varepsilon_{\rm r}$  and  $\mu_{\rm r}$  are the relative permittivity and permeability of the dielectric substrate, respectively, while *k* being the wave number of the incident EM wave in the dielectric medium which in turn can be written as  $k = k_0 \sqrt{\varepsilon_{\rm r} \mu_{\rm r}}$ .

When an EM wave is incident on any interface separating two media, the reflection and transmission of the wave are mainly governed by the intrinsic impedances of the two media. If there is no transmission due to complete metal backing, the absorption of the incident wave in the second medium can be maximized by minimizing the reflectivity only and that can be possible by proper admittance matching at the interface as observed from (2). As the input admittance ( $Y_{in}$ ) of the proposed structure is the parallel combination of the top FSS admittance ( $Y_{FSS}$ ) and the terminated dielectric admittance ( $Y_d$ ), the susceptance of  $Y_{FSS}$  has to be cancelled out by the susceptance of  $Y_d$ to make the overall admittance ( $Y_{in}$ ) real and only then the input



**Figure 4** Reflection coefficient for different chip resistor ( $R_{\rm C}$ ) values of the proposed broadband absorber. [Color figure can be viewed in the online issue, which is available at wileyonlinelibrary.com]

admittance will match the free space admittance, thus causing near-unity absorption as observed from (3)–(7).

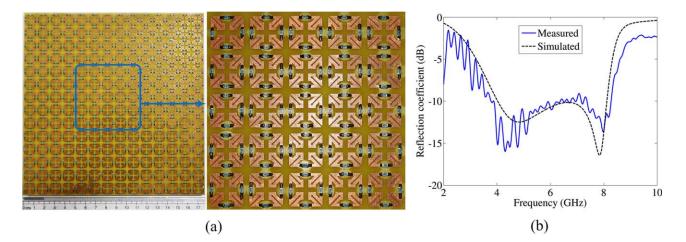
The real and imaginary parts of the normalized input admittance (calculated from simulated reflection coefficient) are plotted in Figure 2(b) and it is observed that  $Im(y_{IN})$  to be close to 0 for the entire frequency range of 4–8.12 GHz thus satisfying the above conditions for admittance matching. Although the proposed structure provides two reflection dips, one below the Salisbury screen zone and another above the Salisbury screen zone [13], the observed bandwidth is very large instead of two narrow absorption peaks at these two frequencies (4.82 and 7.84 GHz). This broad absorption band has been realized by the surface mounting lumped resistors, where the resistors absorb the incident EM wave energy, thus realizing broadband.

The proposed absorber is four-fold symmetric by design and, therefore, polarization insensitive as verified from Figure 3(a). The reflectivity of the structure is further studied for different incident angles upto  $60^{\circ}$  under transverse electric (TE) polarization as shown in Figure 3(b), which exhibits good absorption  $(|S_{11}| < -7.5 \text{ dB})$  upto  $45^{\circ}$  angle of incidence.

To examine the effect of the chip resistors on the designed absorber, the simulated reflection coefficients for different values of resistors are plotted in Figure 4. It is observed that as the resistance value increases, the full width at half maximum bandwidth of the structure decreases gradually. This can be explained from the parallel resonant circuit shown in Figure 2(a), where the quality factor (Q) of the structure increases proportionally with the resistance value ( $R_{\rm eff}$ ). However, the reflection coefficient of the structure gradually increases with decreasing value of resistors. Therefore, an optimum value of resistor ( $R_{\rm C} = 150$   $\Omega$ ) has been chosen which gives rise to wide absorption bandwidth as well as maintains -10 dB reflection level throughout the desired frequency range.

# 3. EXPERIMENTAL RESULTS

To experimentally verify the proposed broadband absorber, the structure is fabricated on the top surface of FR-4 substrate having dimensions of 175 mm  $\times$  175 mm  $\times$  5 mm with complete copper lamination at back. Then the chip resistors of 150  $\Omega$  are soldered on the fabricated design in surface-mounting technology. The fabricated structure, consisting of 14  $\times$  14 unit cells and a total of 784 chip resistors, is depicted in Figure 5(a) along with its enlarged view.



**Figure 5** (a) Photograph of the fabricated absorber with enlarged view showing  $5 \times 5$  array of unit cells, and (b) comparison of the simulated and measured reflection coefficients of the proposed broadband absorber structure. [Color figure can be viewed in the online issue, which is available at wileyonlinelibrary.com]

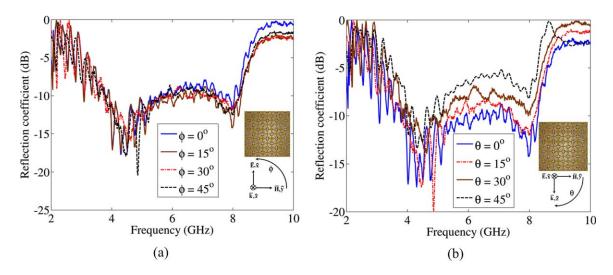
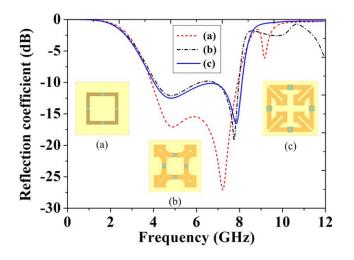


Figure 6 Measured reflection coefficients of the fabricated structure for different angles of (a) polarization under normal incidence and (b) oblique incidence under TE polarization. [Color figure can be viewed in the online issue, which is available at wileyonlinelibrary.com]



**Figure 7** Comparison of reflection coefficients among different shaped broadband absorbers: (a) Single square loop, (b) Minkowski geometry, and (c) Proposed one. [Color figure can be viewed in the online issue, which is available at wileyonlinelibrary.com]

A pair of broadband (1-18 GHz) horn antennas is used along with Agilent N5230A network analyzer to measure the reflections from the fabricated sample. The distance between the structure and the antenna pair is adjusted so that far-field condition is maintained throughout the experiment and the whole experiment set up is placed inside the anechoic chamber. Initially, the reflection coefficient from a metal plate of identical dimension has been recorded and stored as a reference value. Then the reflection from the fabricated structure has been measured and the difference between the two reflected values gives the actual reflection coefficient of the structure. This differential measurement process minimizes the edge diffraction, path loss and any other anomaly present in the measurement process. The measured reflectivity is observed in Figure 5(b), which is below -10 dB for the frequency range of 3.78-8.28 GHz covering the entire C-band. Two minima are also observed at 4.31 and 7.98 GHz, with reflection coefficients of -15.97 and -13.64 dB, respectively.

The fabricated structure is examined for its polarization insensitive behavior under normal incidence by rotating the sample around its own axis in steps of  $15^{\circ}$  upto  $45^{\circ}$  and the

TABLE 1 Summary of design, geometry and size

Absorber Designs	Centre Frequency (GHz)	Unit Cell Size (mm)	Thickness (mm)	-10 dB Bandwidth (GHz)
Single square loop (a)	6	20 (0.4λ <sub>0</sub> )	6 (0.12λ <sub>0</sub> )	48
Minkowski geometry (b)	6.05	16.5 (0.33λ <sub>0</sub> )	5 (0.10λ <sub>0</sub> )	4.12–7.97
Proposed one (c)	6.06	12.5 $(0.25\lambda_0)$	5 $(0.10\lambda_0)$	4.03-8.11

reflection coefficient is found to be almost similar in each case as observed from Figure 6(a). Finally, the reflectivity of the structure for different incident angles under TE polarization is measured and is depicted in Figure 6(b), which validates the simulated results in good agreement.

# 4. CONCLUSION

A broadband ultrathin metamaterial absorber has been presented based on lumped resistors. The proposed design is polarization insensitive, wide angle absorptive (upto 45° for TE polarization) and the absorption range covers the entire C-band (4-8.12 GHz) with above 90% absorption level. The absorber has been fabricated and the simulated responses have been verified with the measured results. The proposed design is based on first-order Minkowski fractal geometry, where the tetra arrows have been inverted to make the geometry compact. The proposed structure has also been compared with other broadband absorbers (standard single square loop and Minkowski fractal geometry) operating in C-band as shown in Figure 7 and the compactness of the designed geometry has been clearly observed in Table 1. Therefore, the proposed absorber can be used in several potential applications like radar cross section (RCS) reduction, shielding and in electromagnetic interference/electromagnetic compatibility where the installation dimensions can be significantly reduced.

## ACKNOWLEDGMENT

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# A 460-GHz CMOS SUBSTRATE-INTEGRATED-WAVEGUIDE SLOT-ANTENNA DESIGN

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**ABSTRACT:** A design of a 460 GHz substrate-integrated-waveguide (SIW) slot antenna in a 65-nm CMOS process is presented in thisarticle. The size of the antenna is 380  $\mu$ m  $\times$  224  $\mu$ m. An inductor identification layer is placed in the layout to reduce the internal metal density requirements significantly. The proposed antenna, which includes metal dummy fill to be compliant with the 65 nm process design rules, was simulated using HFSS. The antenna simulated with a 0.5 mm Si substrate has a maximum gain of 0.09 dBi and a radiation efficiency of 29.1%. The bandwidth of S<sub>11</sub> below -10 dB is 25.3 GHz. © 2016 Wiley Periodicals, Inc. Microwave Opt Technol Lett 58:347–351, 2016; View this article online at wileyonlinelibrary.com. DOI 10.1002/mop.29570

**Key words:** *substrate-integrated-waveguide; CMOS; slot antenna; terahertz* 

## 1. INTRODUCTION

Cerro Chajnantor Atacama Telescope (CCAT) will be a nextgeneration sub-millimeter telescope, which is a 25-m telescope located at 5600 m altitude on Cerro Chajnantor, in the Atacama desert of northern Chile [1]. One of the first-light instruments for CCAT is the CCAT Heterodyne Array Instrument (CHAI), which is a dual-frequency band array receiver covering the scientifically most important parts of the 460 and 830 GHz atmospheric windows for simultaneous observations with 64-pixel arrays. This work is a pilot project to investigate the feasibility of a design of a 460 GHz on-chip antenna with the purpose of integrating with entire CHAI receiver on the same integrated circuit. While this work is not officially a part of CCAT CHAI development, it may provide alternatives to cost-effective implementation of CHAI.

There are many different kinds of THz on-chip antennas emerging in the past years: slot [2], dipole [3], patch [4], Yagi-Uda [5], ring [6], and so on. A 434-GHz substrate-integrated-waveguide (SIW) slot antenna with a transmitter/receiver chipset was fabricated in SiGe BiCMOS process, which had a simulated gain of -0.55 dBi and efficiency of 49.8% [2]. A 0.54-THz